

12/14/01

A TWO TRANSISTOR FLASH MEMORY CELL FOR USE IN EEPROM ARRAYS WITH A PROGRAMMABLE

10/016898  
12/14/01



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Explos. Med. No. 1 147-123-1281US

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail, registered mail, and insured mail, and that the addressee is the Assistant Commissioner of Patents, Washington, D.C. 20591. Applicant

**REQUEST AND CERTIFICATION  
UNDER  
35 U.S.C. 122(b)(2)(B)(i)**

First Named Inventor

Hsiang-Ya Tsao

Title

A Two Transistor Flash Memory Cell For  
Use In EEPROM Arrays With A Programmable  
Logic Device

Atty Docket Number

AP01-008

I hereby certify that the invention disclosed in the attached application **has not and will not** be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

12/14/01  
Date



Signature

Stephen B. Ackerman  
Typed or printed name

#37,761

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).